

REMARKS

This application is a divisional application of U.S. Patent Application Serial Number 10/211,886 filed on August 1, 2002, which is a continuation-in-part of U.S. Patent Application Serial number 09/929,542 filed on August 13, 2001, which is a divisional of U.S. Patent Application no. 09/231,928 filed on January 14, 1999 which was issued on August 28, 2001 as US Patent No. 6,282,145.

The specification has been amended to correct minor typographical errors. No new matter has been added.

Paragraph 0226 has been amended to change [memory cell 2800] to read memory system 2800 as described in paragraph 0225.

Paragraph 0245 has been amended to change [MOS transistor 3174] to read PMOS transistor 3176. Reference numeral 3174 corresponds to global sense amplifier 3174. Figure 31 has been changed to reflect this amendment. A copy of corrected Figure 31 is attached with the change circled in red.

Paragraph 0256 has been amended to correct a grammatical error and a typographical error.

Claims 1-40, 42-49, 57-60, 68, 70, 75, 80, 84, 88-126 have been cancelled.

Amendments to Figures 32 and 33 are reflected on the attached replacements sheets. Figures 22c and 28-35 are also attached as formal drawings.

Please address all future communications regarding this application to:

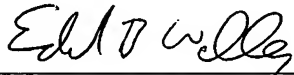
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Please charge any additional fees, including any fees necessary for extensions of time, or credit overpayment to Deposit Account No. 07-1896 referencing our Docket No. 2102475-912332.

Dated: January 22, 2004

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By 
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Mark up Drawings

FIG. 31

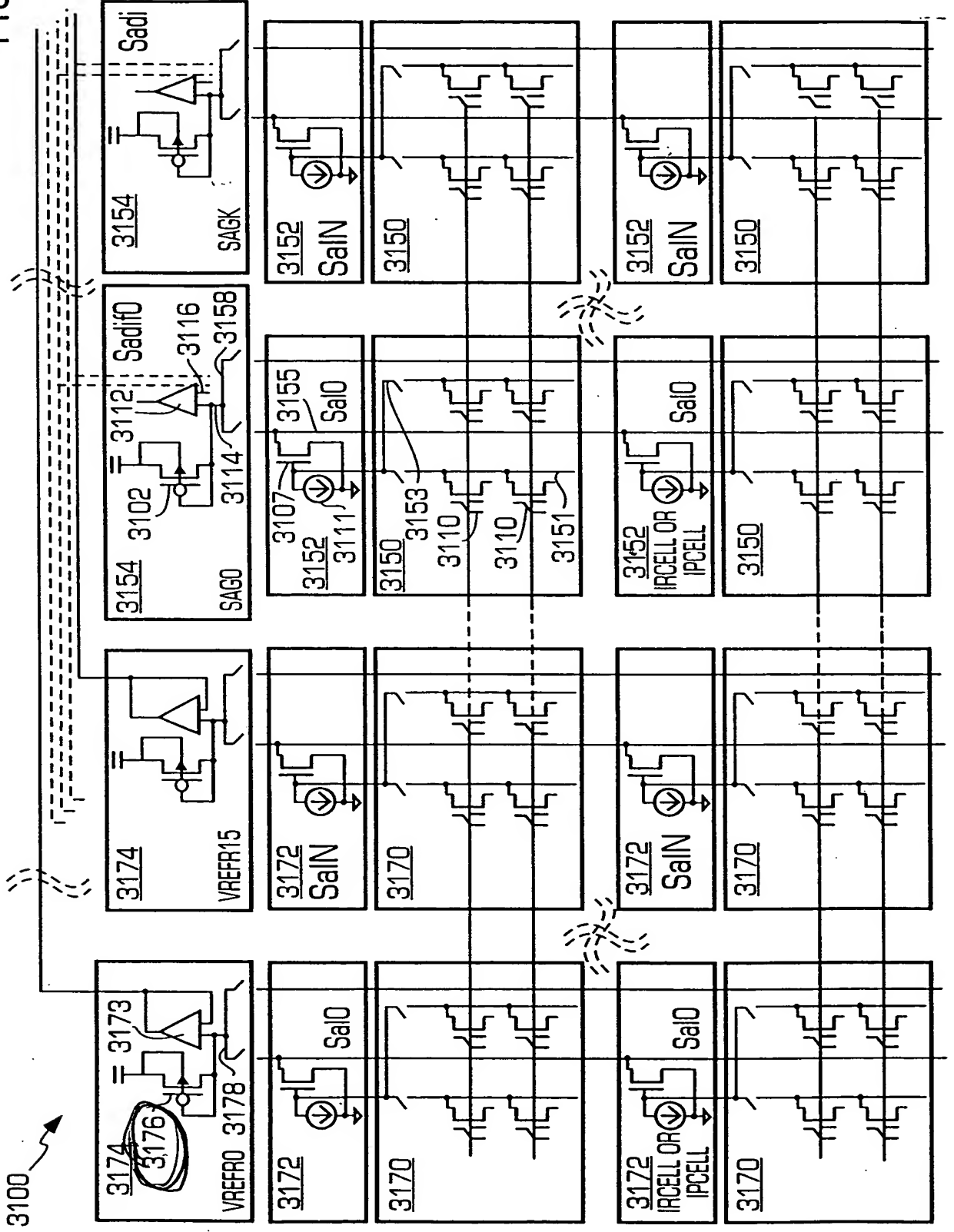


FIG. 32

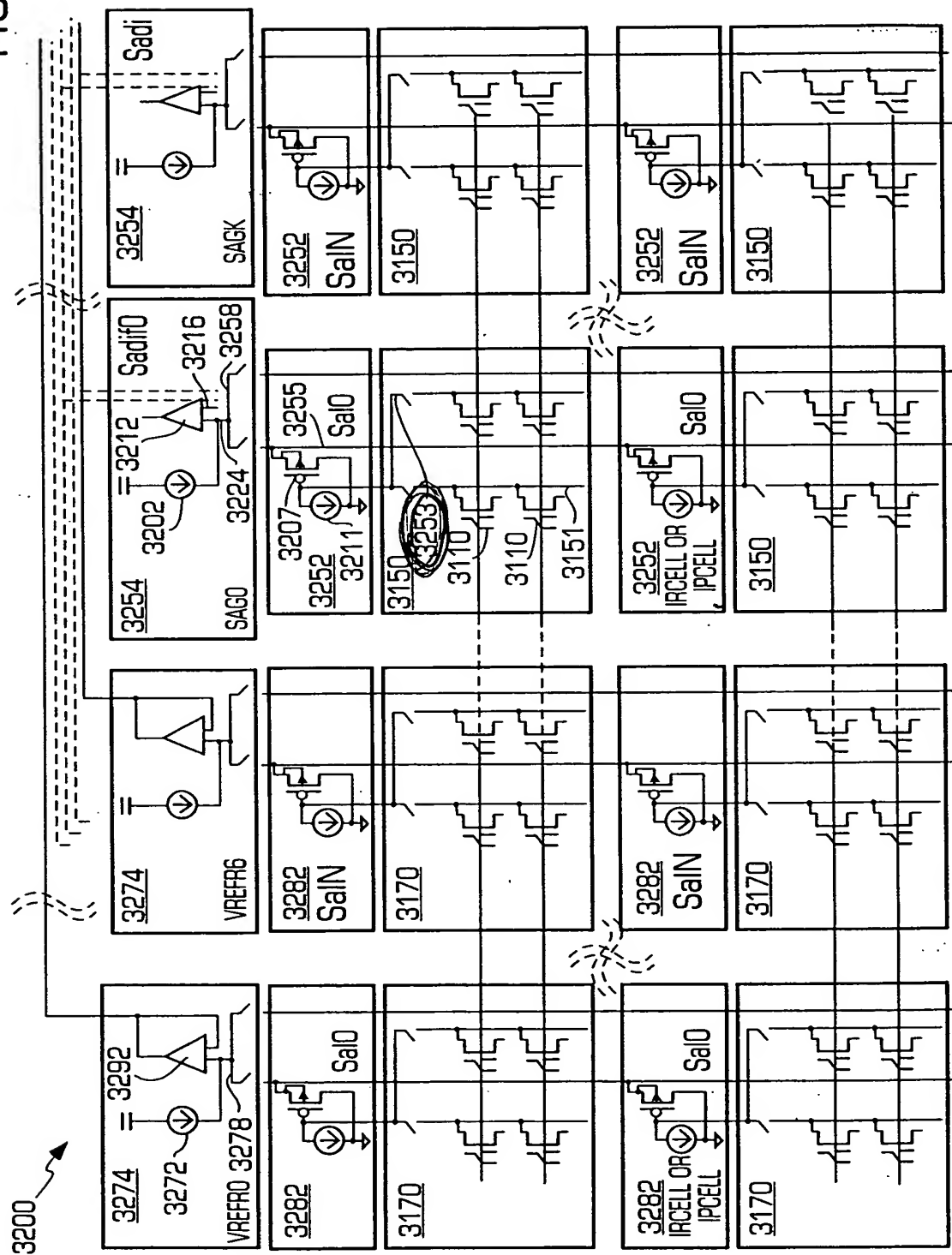
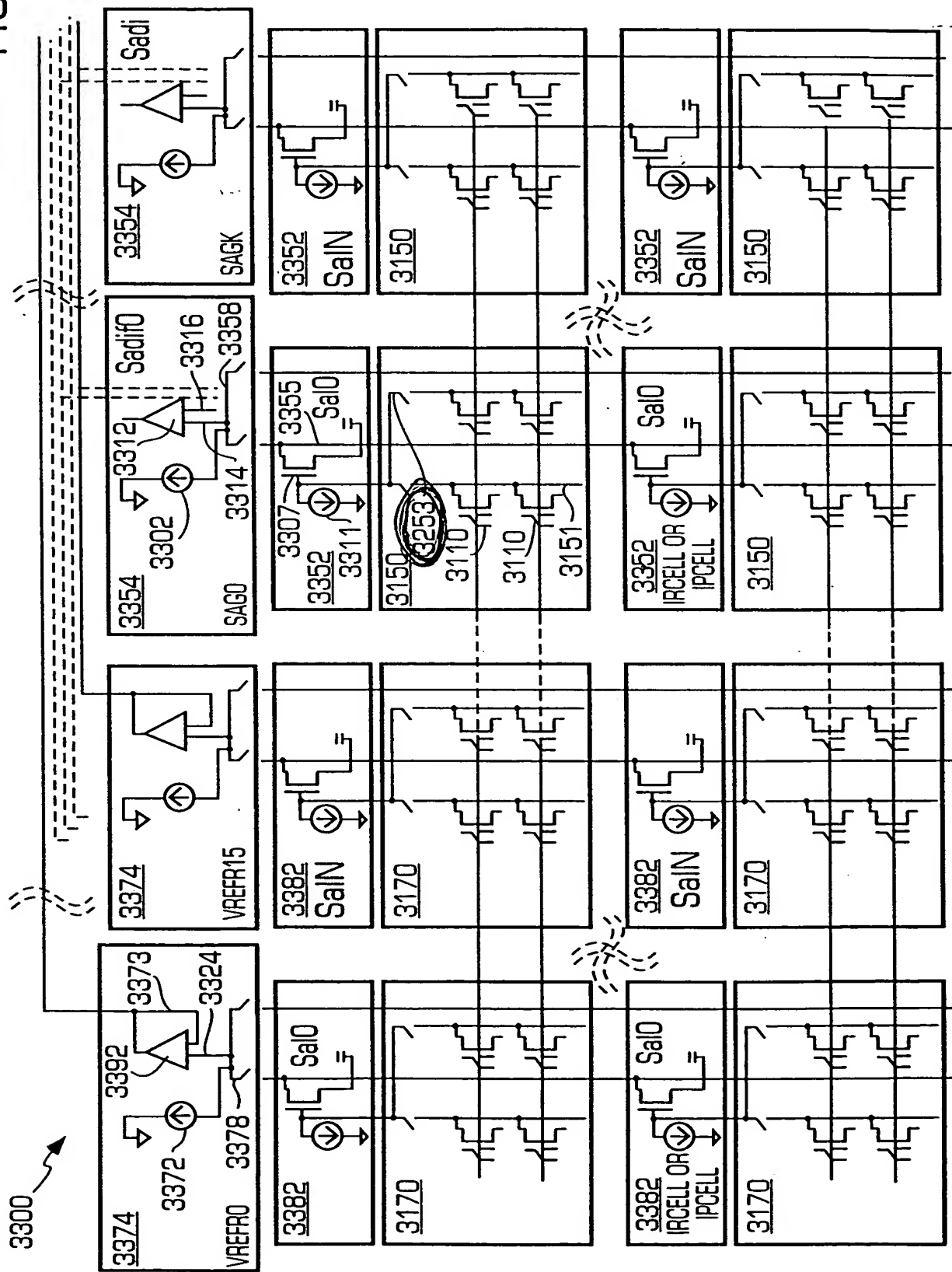


FIG. 33



REPLACEMENT DRAWINGS

REPLACEMENT DRAWINGS

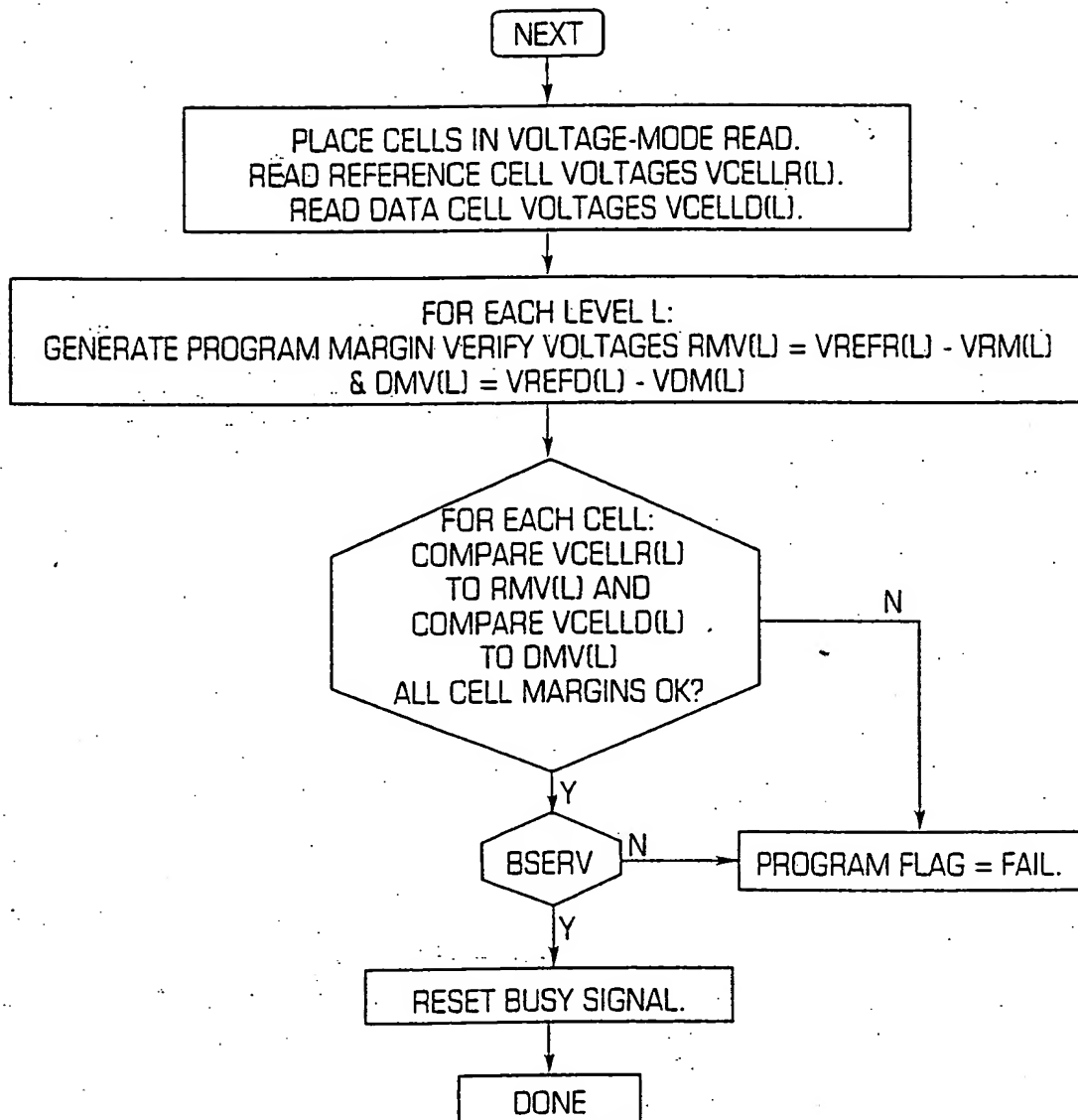


FIG. 22C

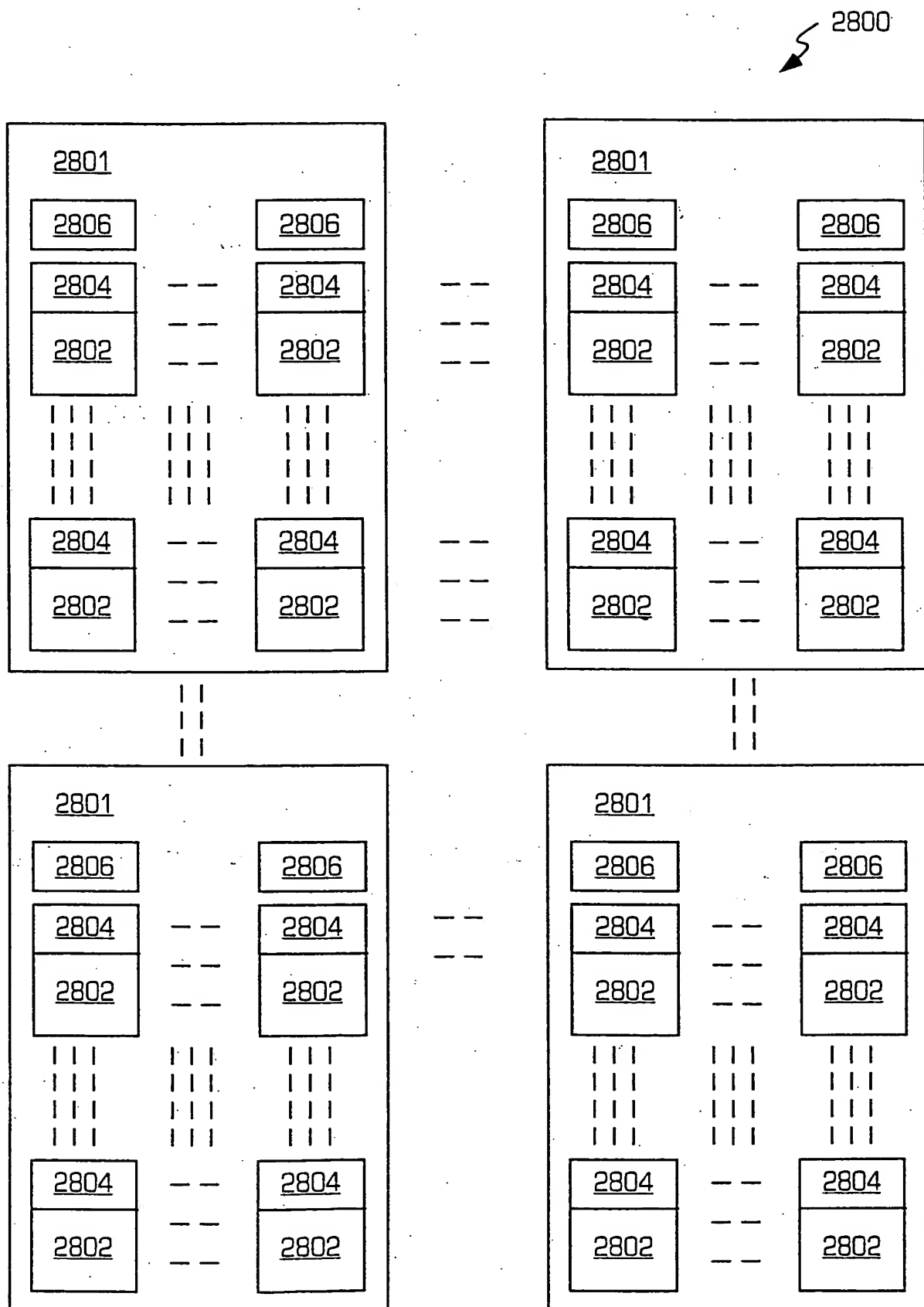


FIG. 28

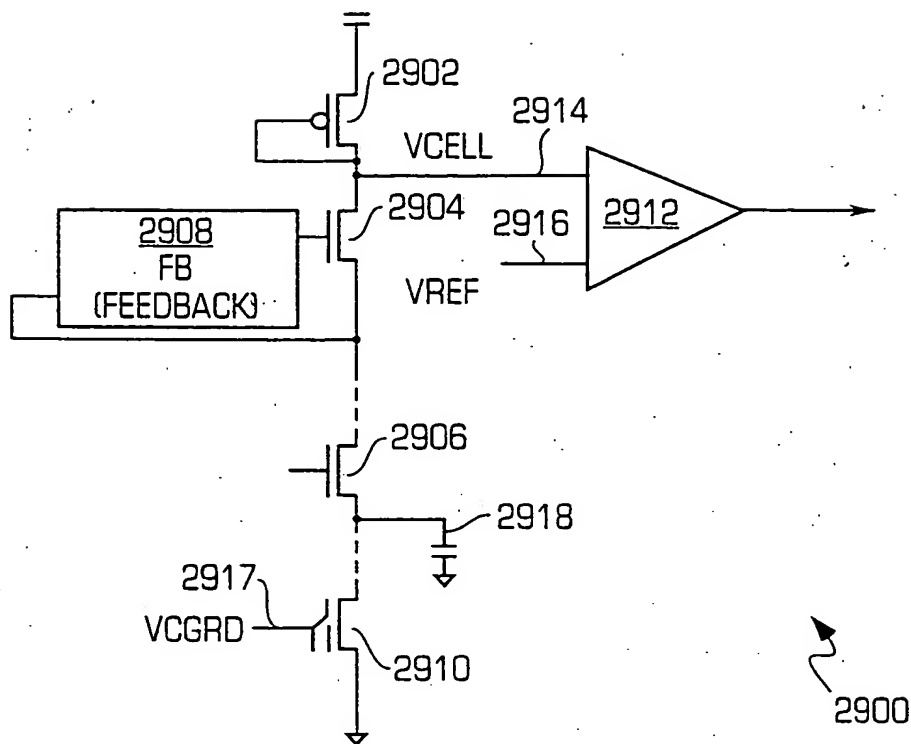


FIG. 29A

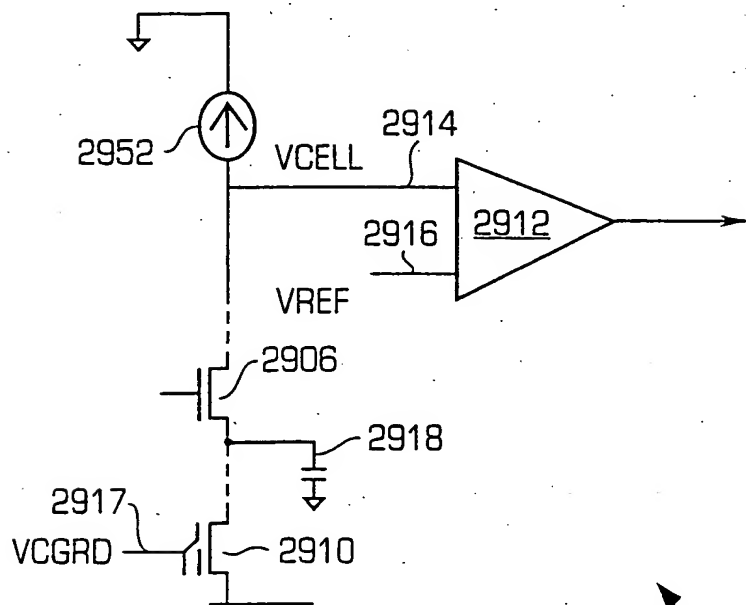


FIG. 29B

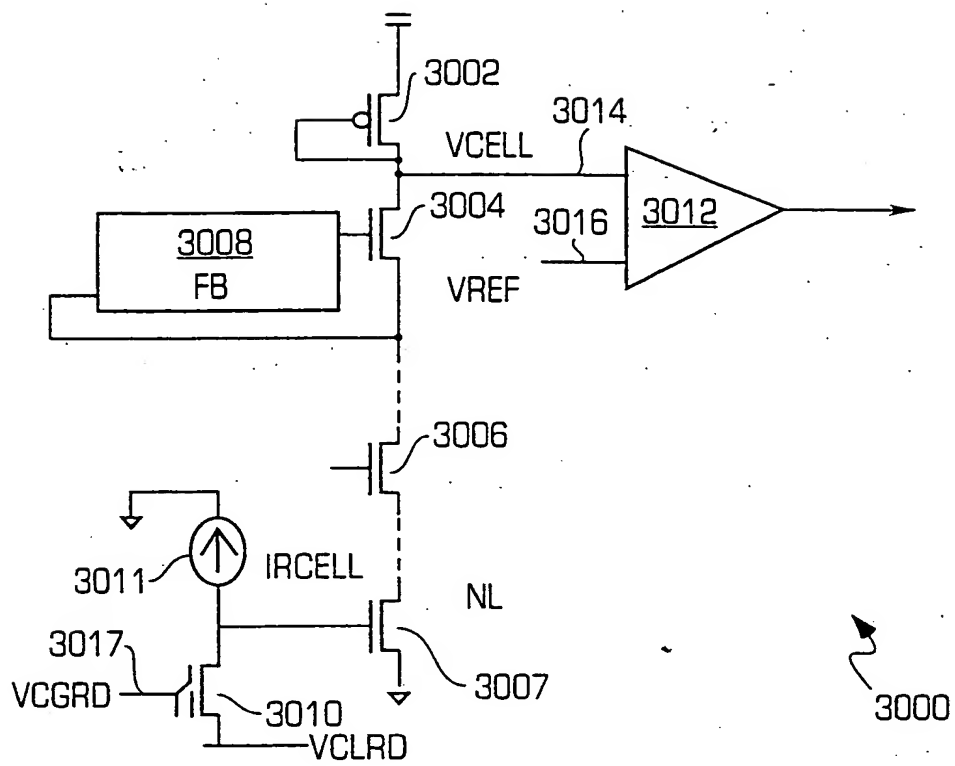


FIG. 30

FIG. 31

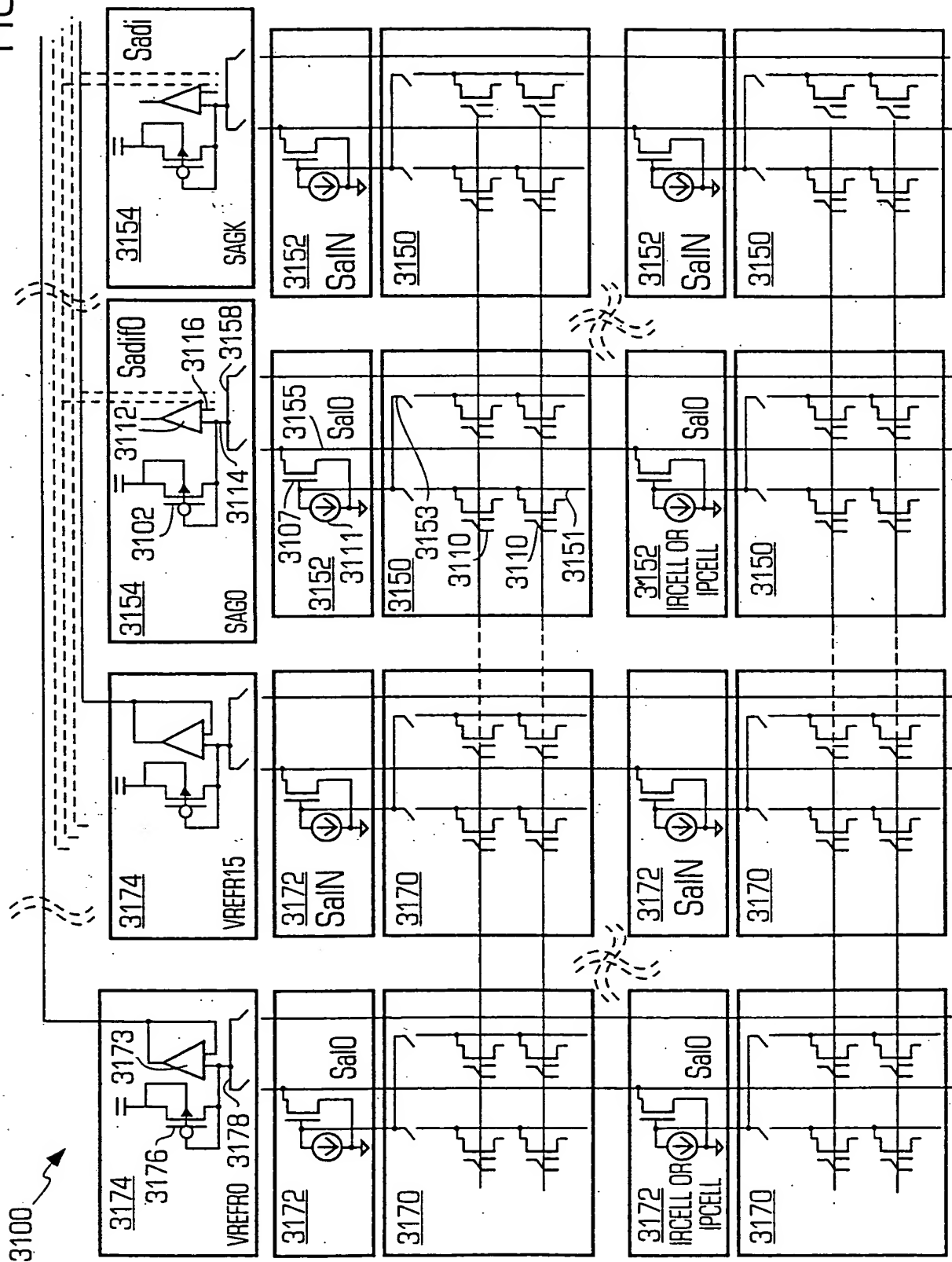


FIG. 32

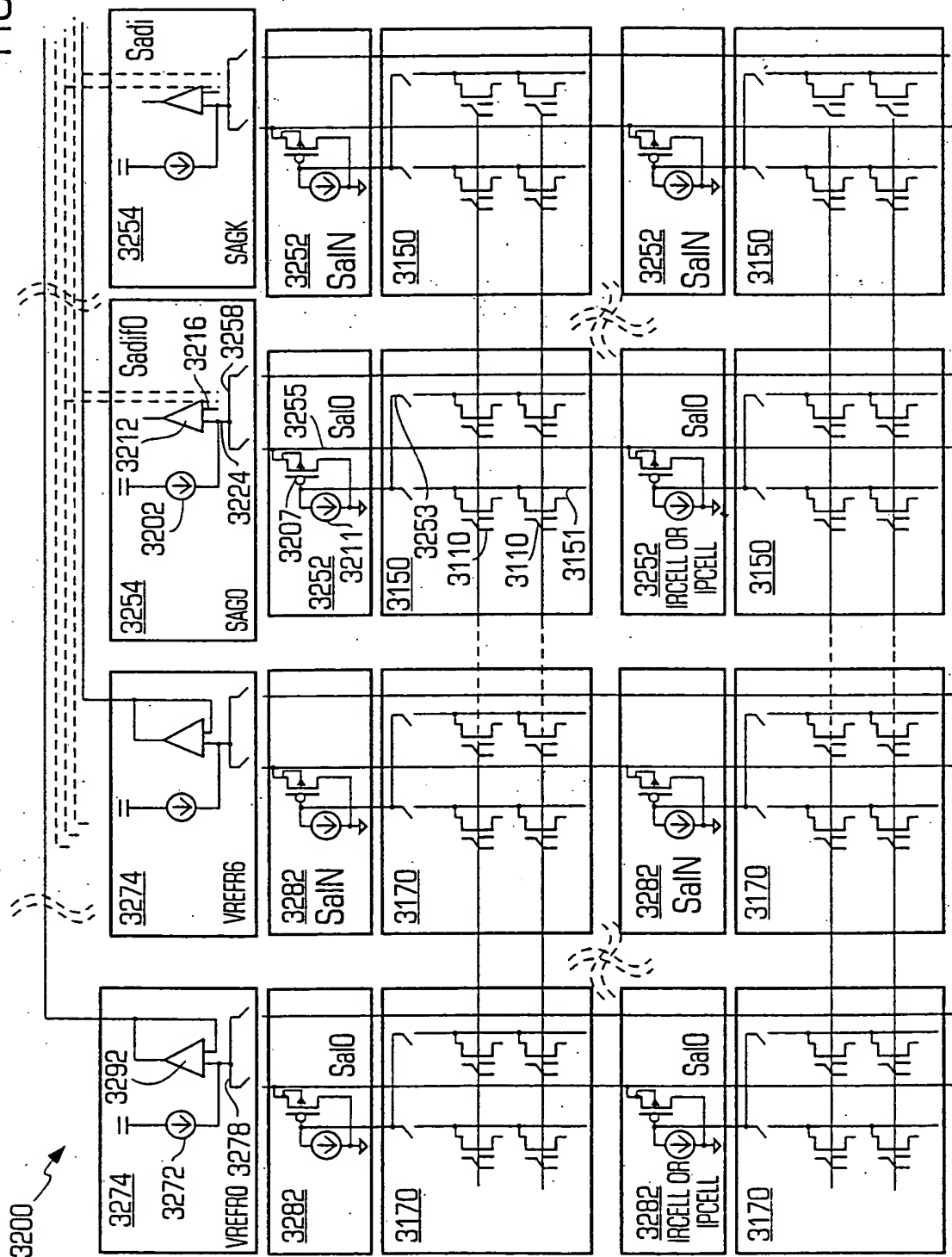
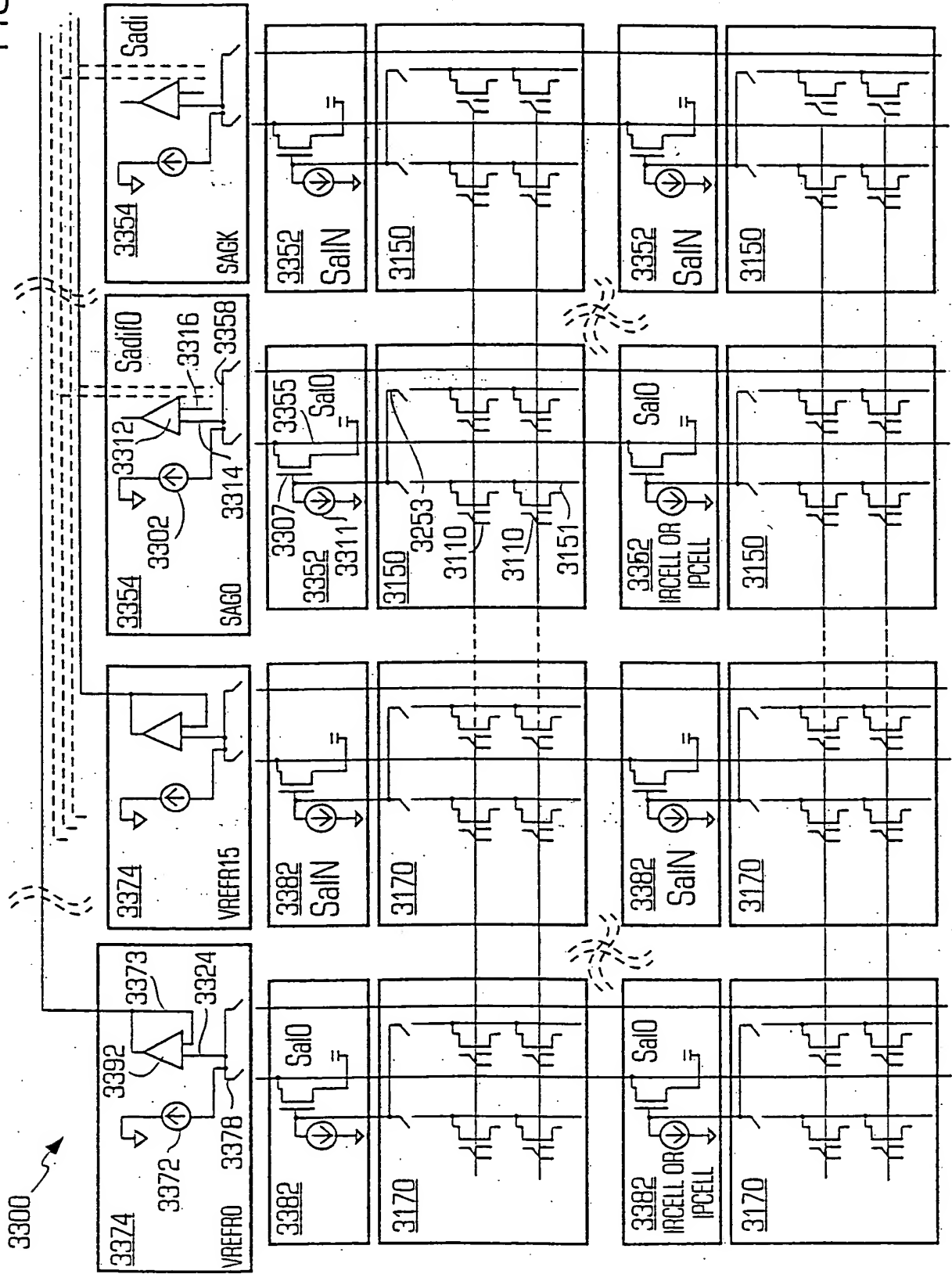


FIG. 33



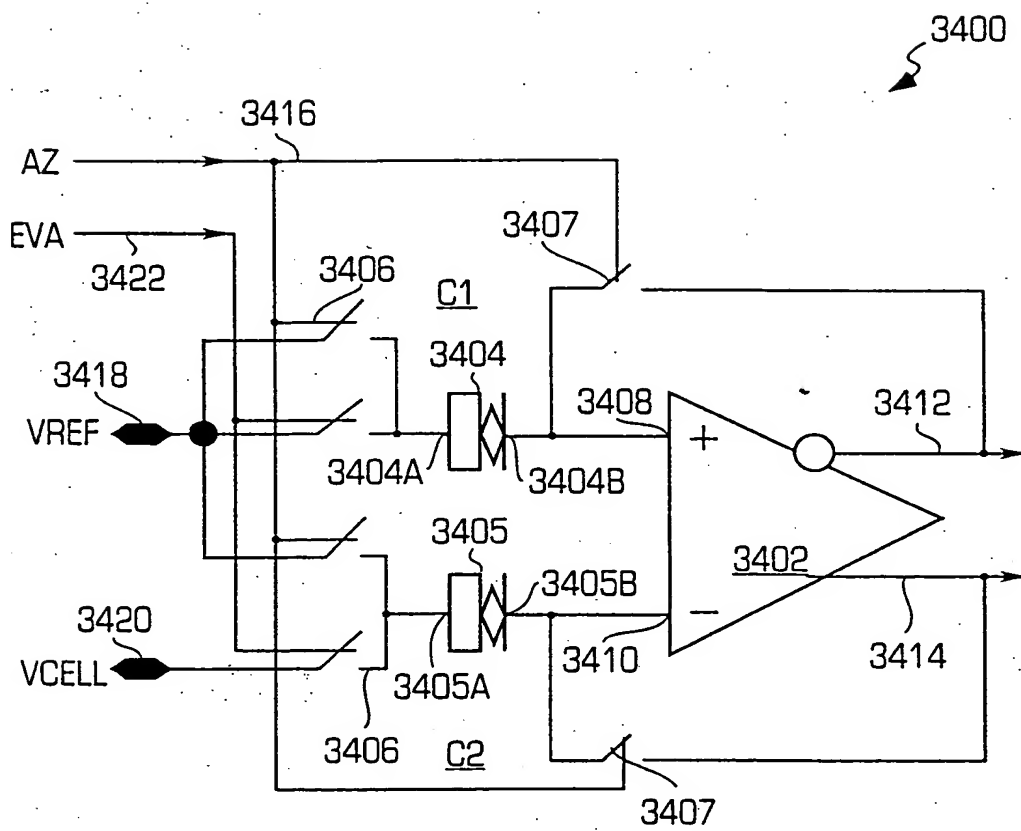


FIG. 34

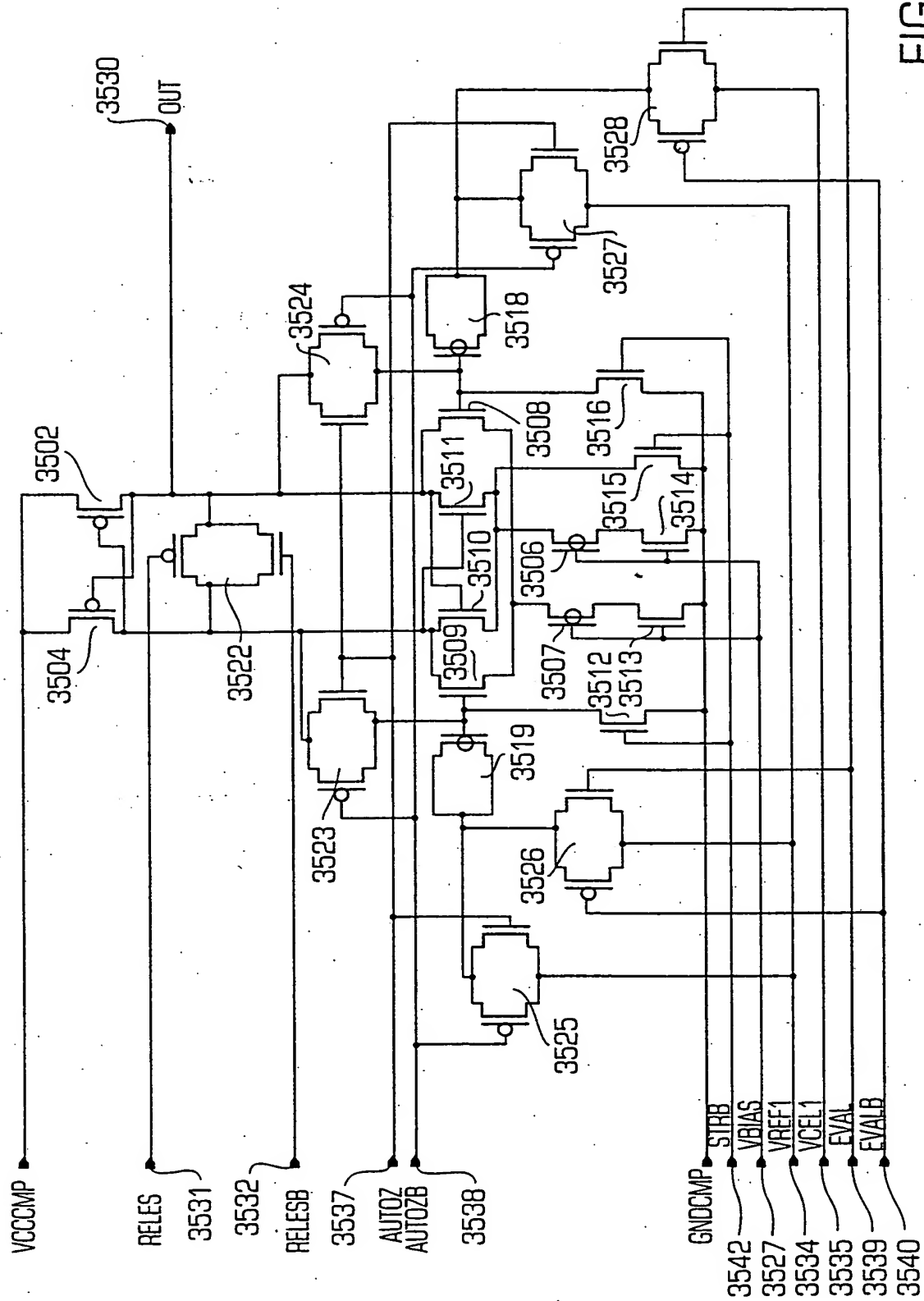


FIG. 35